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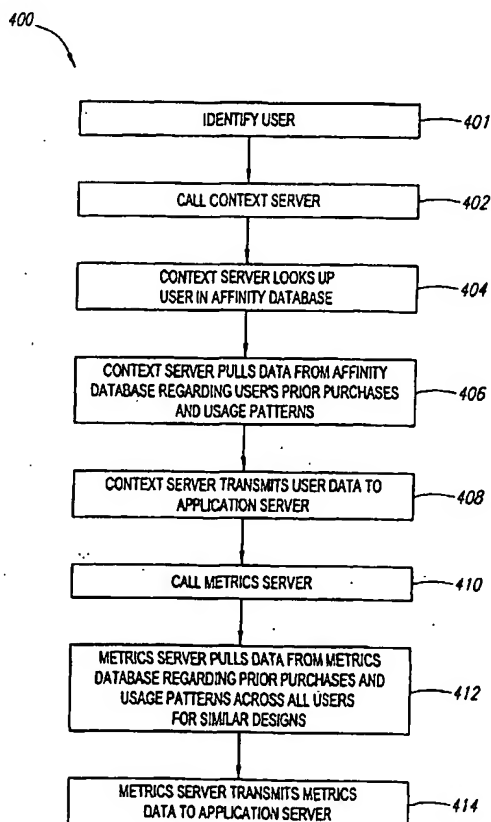
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(54) Title: METHOD AND SYSTEM FOR FACILITATING ELECTRONIC CIRCUIT AND CHIP DESIGN USING REMOTELY LOCATED RESOURCES



(57) Abstract: A multi-faceted portal site acts as a server in the context of an n-tier client/ server network, and connects electronic designers and design teams to design and verification tool and service providers on the other through a single portal site. Tools and services accessible to users through the portal site include electronic design automation (EDA) software tools, electronic component information, electronic component databases of parts (or dynamic parts), computing and processing resources, virtual circuit blocks, design expert assistance, and integrated circuit fabrication. Such tools and services may be provided in whole or part by suppliers connected to the portal site. Users accessing the portal site are presented with options in a menu or other convenient format identifying the tools and services available, and are able to more rapidly complete circuit designs by having access to a wide variety of tools and services in a single locale. The portal site may facilitate purchase, lease or other acquisition of the tools and services offered through it. The portal site tracks the movements of users through the portal site in order to learn about the design preferences and design approaches of users individually and in the aggregate. Previous actions taken by the user and by similarly-situated users may be considered in determining which information presented to the user, or in what order to present information to the user, thereby providing contextually-driven access.

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## INTERNATIONAL SEARCH REPORT

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**A. CLASSIFICATION OF SUBJECT MATTER**  
 IPC 7 G06F17/50

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, IBM-TDB, PAJ

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

| Category * | Citation of document, with indication, where appropriate, of the relevant passages  | Relevant to claim No. |
|------------|---|-----------------------|
| X          | PERRIN B: "WEB-BASED CIRCUIT ENGINEERING"<br>CIRCUIT CELLAR INK, CIRCUIT CELLAR INC.,<br>VERNON, CT., US,<br>no. 104, March 1999 (1999-03), pages<br>28-30, 33, 35, 37-38, XP001034444<br>ISSN: 0896-8985 | 1, 7-15,<br>17-49     |
| Y          | page 30, column 3<br><br>page 35, column 1<br><br>-----<br>-/-  | 2-6, 16,<br>50-53     |

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages   | Relevant to claim No. |
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| X          | BENINI L ET AL: "Distributed EDA tool integration: the PPP paradigm"<br>PROCEEDINGS OF THE INTERNATIONAL CONFERENCE ON COMPUTER DESIGN. ICCD. VLSI IN COMPUTERS AND PROCESSORS. AUSTIN, OCT. 7 - 9, 1996, LOS ALAMITOS, IEEE COMP. SOC. PRESS, US,<br>7 October 1996 (1996-10-07), pages 448-453, XP010201836<br>ISBN: 0-8186-7554-3 | 13, 17, 18            |
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# INTERNATIONAL SEARCH REPORT

Information on patent family members

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| Patent document<br>cited in search report |   | Publication<br>date | Patent family<br>member(s) | Publication<br>date |
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| EP 0944002                                | A | 22-09-1999          | EP 0944002 A1              | 22-09-1999          |
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